

V_{DRM}	=	6000 V
I_{TGQM}	=	3000 A
I_{TSM}	=	27.5×10^3 A
$V_{(T0)}$	=	1.5 V
r_T	=	0.65 mW
V_{DClink}	=	3600 V

Asymmetric Integrated Gate-Commutated Thyristor

5SHY 30L6010

Doc. No. 5SYA1239-01 March 05

- High snubberless turn-off rating
- Optimized for medium frequency (<1kHz) and low turn-off losses
- High reliability
- High electromagnetic immunity
- Simple control interface with status feedback
- AC or DC supply voltage
- Contact factory for series connection



Blocking

*Maximum rated values*¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Rep. peak off-state voltage	V_{DRM}	Gate Unit energized			6000	V
Permanent DC voltage for 100 FIT failure rate of GCT	V_{DClink}	Ambient cosmic radiation at sea level in open air. Gate Unit energized			3600	V
Reverse voltage	V_{RRM}	IGCT in off-state			17	V
		IGCT in on-state			10	V

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Rep. peak off-state current	I_{DRM}	$V_D = V_{DRM}$, Gate Unit energized			50	mA

Mechanical data (see Fig. 11, 12)

*Maximum rated values*¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	F_m		36	40	44	kN

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Pole-piece diameter	D_p	± 0.1 mm		85		mm
Housing thickness	H		25.5		25.9	mm
Weight	m				2.9	kg
Surface creepage distance	D_s	Anode to Gate	33			mm
Air strike distance	D_a	Anode to Gate	10			mm
Length	l	± 1.0 mm		439		mm
Height	h	± 1.0 mm		40		mm
Width IGCT	w	± 1.0 mm		173		mm

1) Maximum rated values indicate limits beyond which damage to the device may occur

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GCT Data

On-state (see Fig. 3, 4, 5, 6, 14, 15)

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. average on-state current	$I_{T(AV)M}$	Half sine wave, $T_C = 85\text{ °C}$, Double side cooled			1300	A
Max. RMS on-state current	$I_{T(RMS)}$				2000	A
Max. peak non-repetitive surge on-state current	I_{TSM}	$t_p = 10\text{ ms}$, $T_j = 125\text{ °C}$, sine wave after surge: $V_D = V_R = 0\text{ V}$			27.5×10^3	A
Limiting load integral	I^2t				3.8×10^6	A^2s
Max. peak non-repetitive surge on-state current	I_{TSM}	$t_p = 30\text{ ms}$, $T_j = 125\text{ °C}$, sine wave after surge: $V_D = V_R = 0\text{ V}$			20×10^3	A
Limiting load integral	I^2t				6×10^6	A^2s
Stray inductance between GCT and antiparallel diode	L_D	Only relevant for applications with antiparallel diode to the IGCT			300	nH
Critical rate of rise of on-state current	di_T/dt_{cr}	For higher di_T/dt and current lower than 100 A an external retrigger puls is required.			200	A/ μs

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	V_T	$I_T = 3000\text{ A}$, $T_j = 125\text{ °C}$		3.0	3.4	V
Threshold voltage	$V_{(T0)}$	$T_j = 125\text{ °C}$ $I_T = 1000 \dots 5000\text{ A}$			1.5	V
Slope resistance	r_T				0.65	m Ω

Turn-on switching (see Fig. 10, 14, 15)

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Critical rate of rise of on-state current	di_T/dt_{cr}	$f = 0.500\text{ Hz}$, $T_j = 125\text{ °C}$, $I_T = 2000\text{ A}$, $V_D = 3600\text{ V}$			1000	A/ μs

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Turn-on delay time	t_{don}	$V_D = 3600\text{ V}$, $T_j = 125\text{ °C}$ $I_T = 2000\text{ A}$, $di/dt = V_D / L_i$ $L_i = 5\text{ }\mu\text{H}$ $C_{CL} = 10\text{ }\mu\text{F}$, $L_{CL} = 0.3\text{ }\mu\text{H}$			3.5	μs
Turn-on delay time status feedback	$t_{don SF}$				7	μs
Rise time	t_r				1	μs
Turn-on energy per pulse	E_{on}			1	1.5	J

Turn-off switching (see Fig. 7, 8, 10, 14, 15)

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. controllable turn-off current	I_{TGQM}	$V_{DM} \leq V_{DRM}$, $T_j = 125\text{ °C}$, $V_D = 3000\text{ V}$, $R_S = 0.65\text{ }\Omega$, $C_{CL} = 10\text{ }\mu\text{F}$, $L_{CL} \leq 0.3\text{ }\mu\text{H}$,			3000	A

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Turn-off delay time	t_{doff}	$V_D = 3600\text{ V}$, $T_j = 125\text{ °C}$ $V_{DM} \leq V_{DRM}$, $R_S = 0.65\text{ }\Omega$ $I_{TGQ} = 2000\text{ A}$, $L_i = 5\text{ }\mu\text{H}$ $C_{CL} = 10\text{ }\mu\text{F}$, $L_{CL} = 0.3\text{ }\mu\text{H}$,			7	μs
Turn off delay time status feedback	$t_{doff SF}$				7	μs
Turn-off energy per pulse	E_{off}			16.5	20	J

Gate Unit Data

Power supply (see Fig. 2, 9, 10, 12, 13)

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate Unit voltage (Connector X1)	$V_{GIN,RMS}$	AC square wave amplitude (15 kHz - 100kHz) or DC voltage. No galvanic isolation to power circuit.	28		40	V
Min. current needed to power up the Gate Unit	$I_{GIN Min}$	Rectified average current see application note 5SYA 2031	2.1			A
Gate Unit power consumption	$P_{GIN Max}$				100	W

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Internal current limitation	$I_{GIN Max}$	Rectified average current limited by the Gate Unit			8	A

Optical control input/output ^{Note 3}

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Min. on-time	t_{on}		10			μ s
Min. off-time	t_{off}		10			μ s
Min. Switching period	$t_{on} + t_{off}$		60			μ s

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit	
Optical input power	$P_{on CS}$	CS: Control signal SF: Status feedback Valid for 1mm plastic optical fiber (POF)	-15		-1	dBm	
Optical noise power	$P_{off CS}$				-45	dBm	
Optical output power	$P_{on SF}$			-19		-1	dBm
Optical noise power	$P_{off SF}$				-50.0	dBm	
Pulse width threshold	t_{GLITCH}	Max. pulse width without response			400	ns	
External retrigger pulse width	t_{retrig}		600		1100	ns	

Connectors ^{Note 3} (see Fig. 11, 12, 13)

Parameter	Symbol	Description
Gate Unit power connector	X1	AMP: MTA-156, Part Number 641210-5 ^{Note 1}
LWL receiver for command signal	CS	Agilent, Type HFBR-2528 ^{Note 2}
LWL transmitter for status feedback	SF	Agilent, Type HFBR-1528 ^{Note 2}

Note 1: AMP, www.amp.com

Note 2: Agilent Technologies, www.semiconductor.agilent.com

Note 3: Do not disconnect or connect fiber optic cables while Gate Unit is energized.

Visual feedback (see Fig. 13)

Parameter	Symbol	Description	Color
Gate OFF	LED1	LED on when GCT is off	(green)
Gate ON	LED2	LED on when gate-current is flowing	(yellow)
Fault	LED3	LED on when not ready / Failure	(red)
Power supply voltage OK	LED4	LED on when power supply is within specified range	(green)

Thermal

Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Junction operating temperature	T _{vj}		0		125	°C
Storage temperature range	T _{stg}		-40		60	°C
Ambient operational temperature	T _a		0		50	°C

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction-to-case of GCT	R _{th(jc)}	Double side cooled			8.5	K/kW
Thermal resistance case-to-heatsink of GCT	R _{th(ch)}	Double side cooled			3	K/kW

Analytical function for transient thermal impedance:

$$Z_{thJC}(t) = \sum_{i=1}^n R_i(1 - e^{-t/\tau_i})$$

i	1	2	3	4
R _i (K/kW)	5.572	1.533	0.901	0.496
τ _i (s)	0.5151	0.0914	0.0097	0.0027

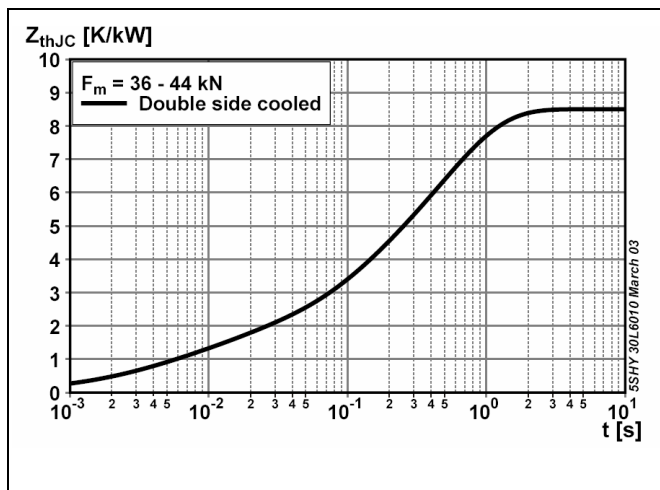


Fig. 1 Transient thermal impedance (junction-to-case) vs. time (max. values).

Max. Turn-off current for Lifetime operation

- Calculated lifetime of on-board capacitors 20 years.
- With slightly forced air cooling (air velocity > 0.5 m/s).
- Strong air cooling allows for increased ambient temperature.

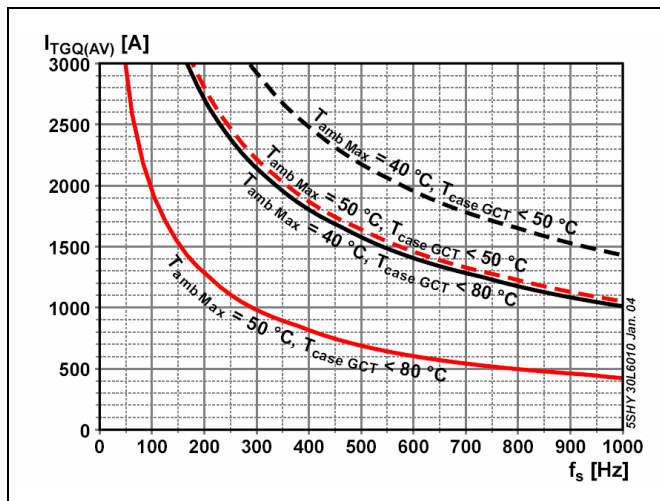


Fig. 2 Max. turn-off current vs. frequency for lifetime operation

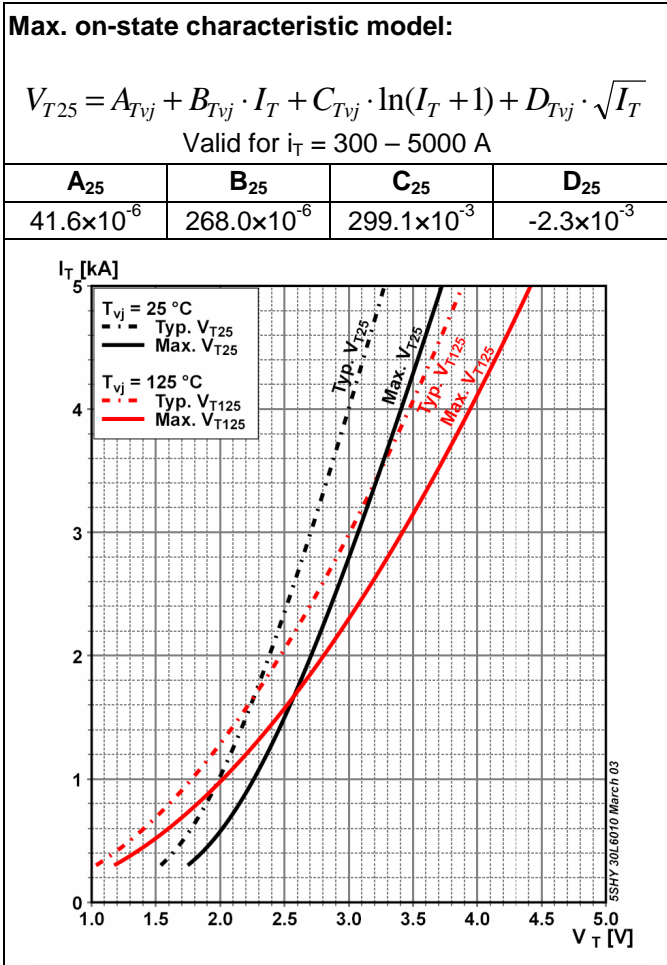


Fig. 3 GCT on-state voltage characteristics

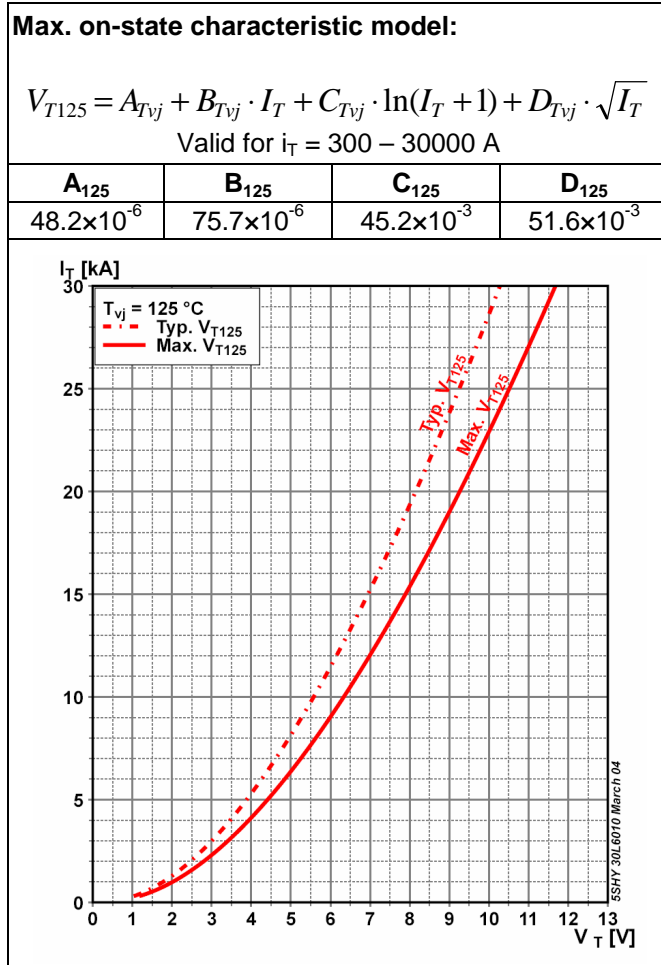


Fig. 4 GCT on-state voltage characteristics

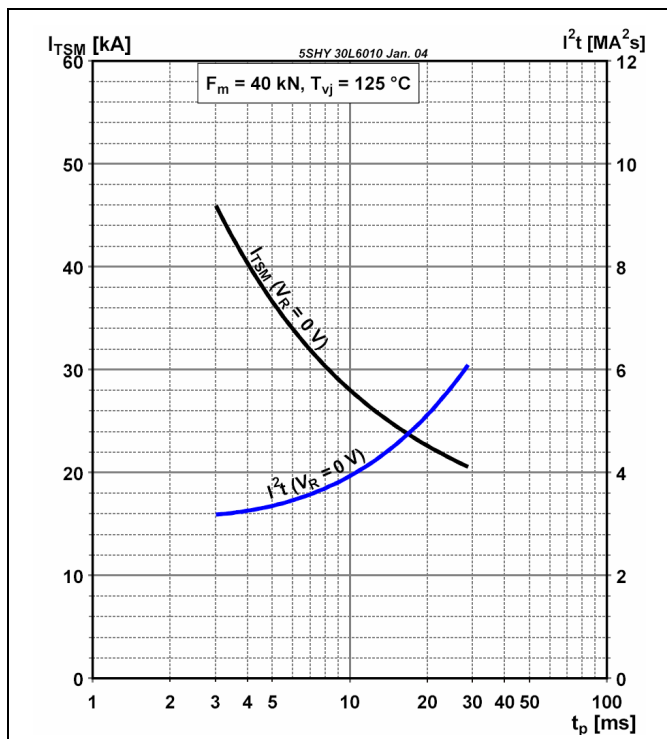


Fig. 5 Surge on-state current vs. pulse length. Half-sine wave.

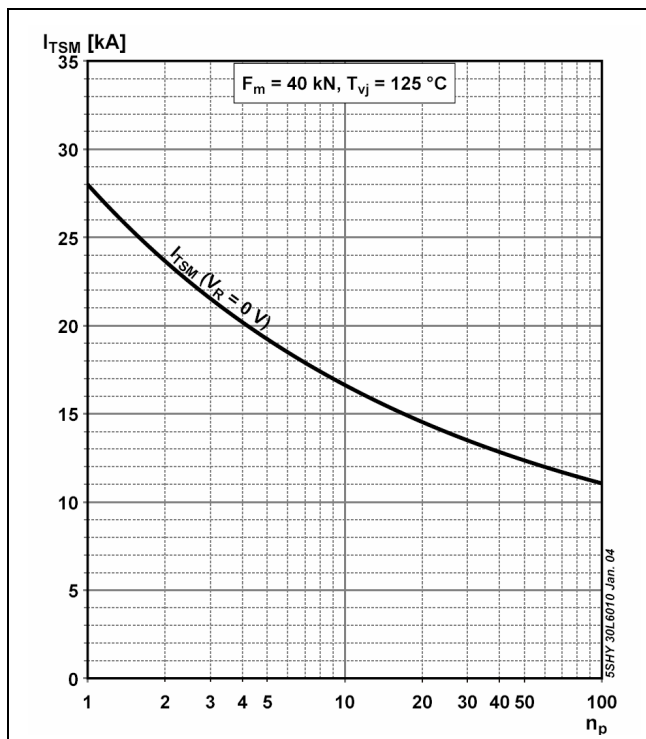


Fig. 6 Surge on-state current vs. number of pulses. Half-sine wave, 10 ms, 50Hz

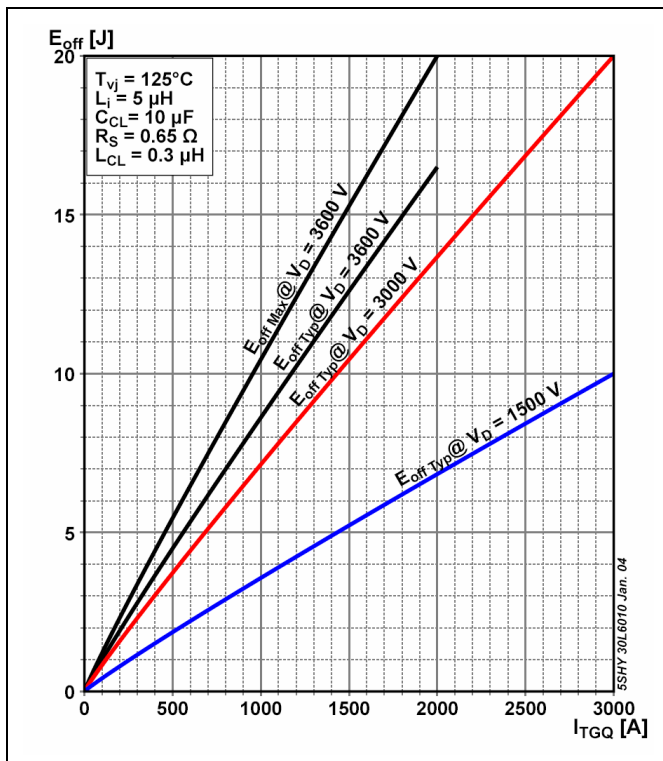


Fig. 7 Typical GCT turn-off energy per pulse vs. turn-off current.

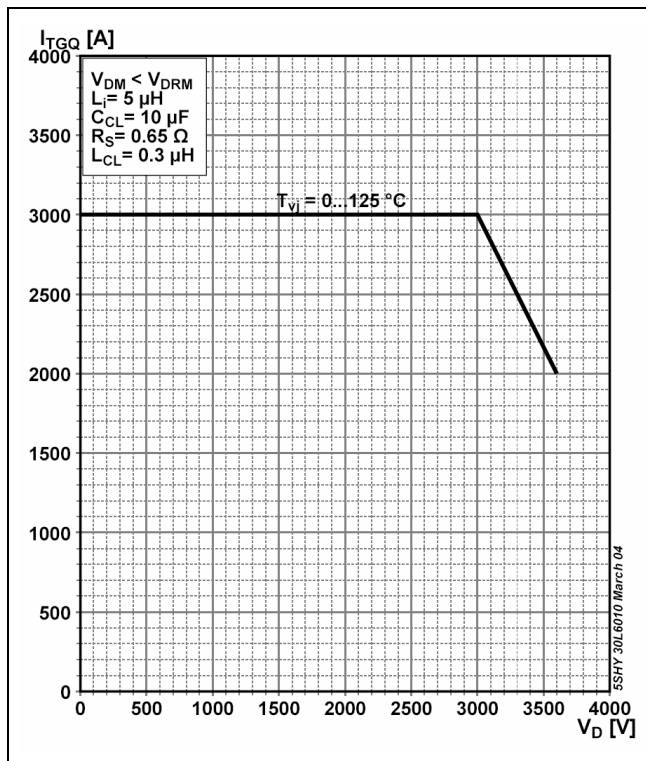


Fig. 8 Safe Operating Area

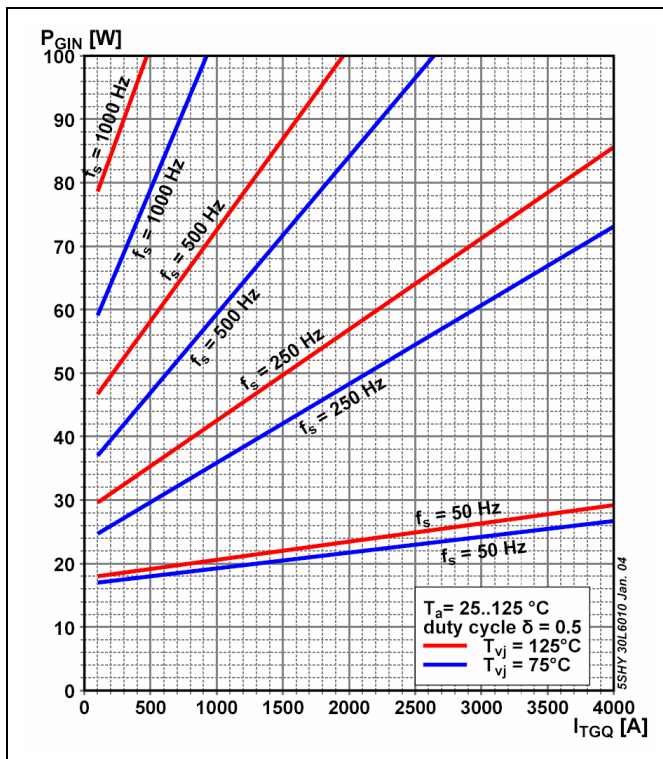


Fig. 9 Max. Gate Unit input power in chopper mode

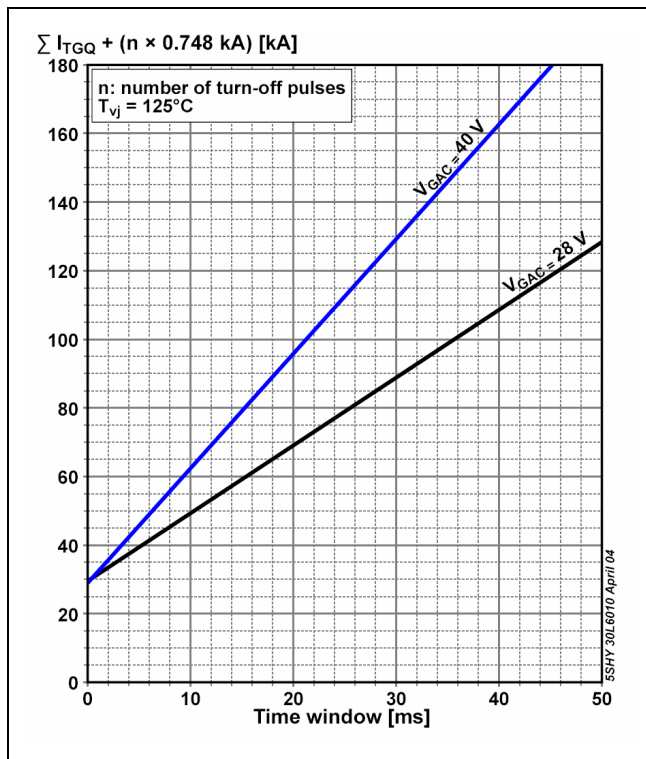


Fig. 10 Burst capability

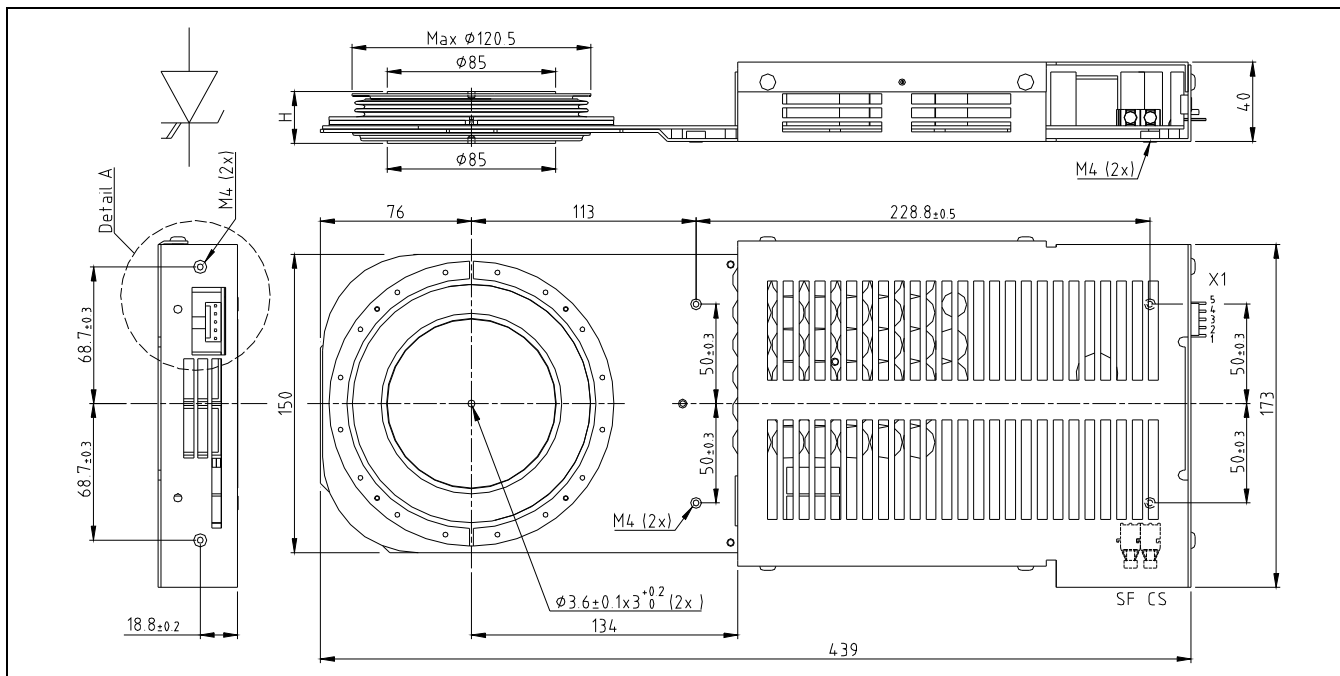


Fig. 11 Outline drawing. All dimensions are in millimeters and represent nominal values unless stated otherwise.

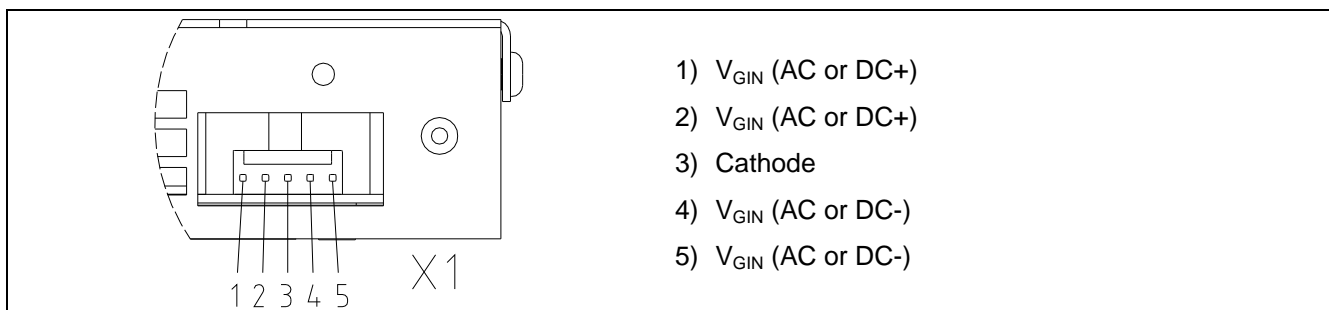


Fig. 12 Detail A: Pin out of supply connector X1.

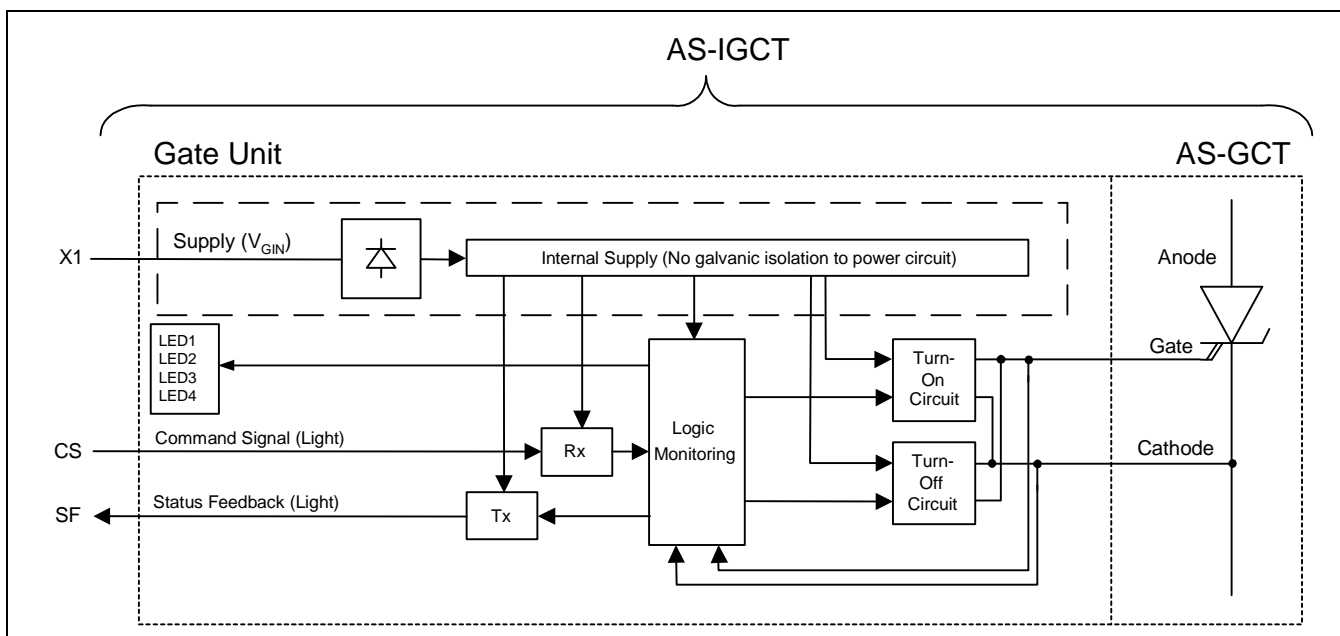


Fig. 13 Block diagram.

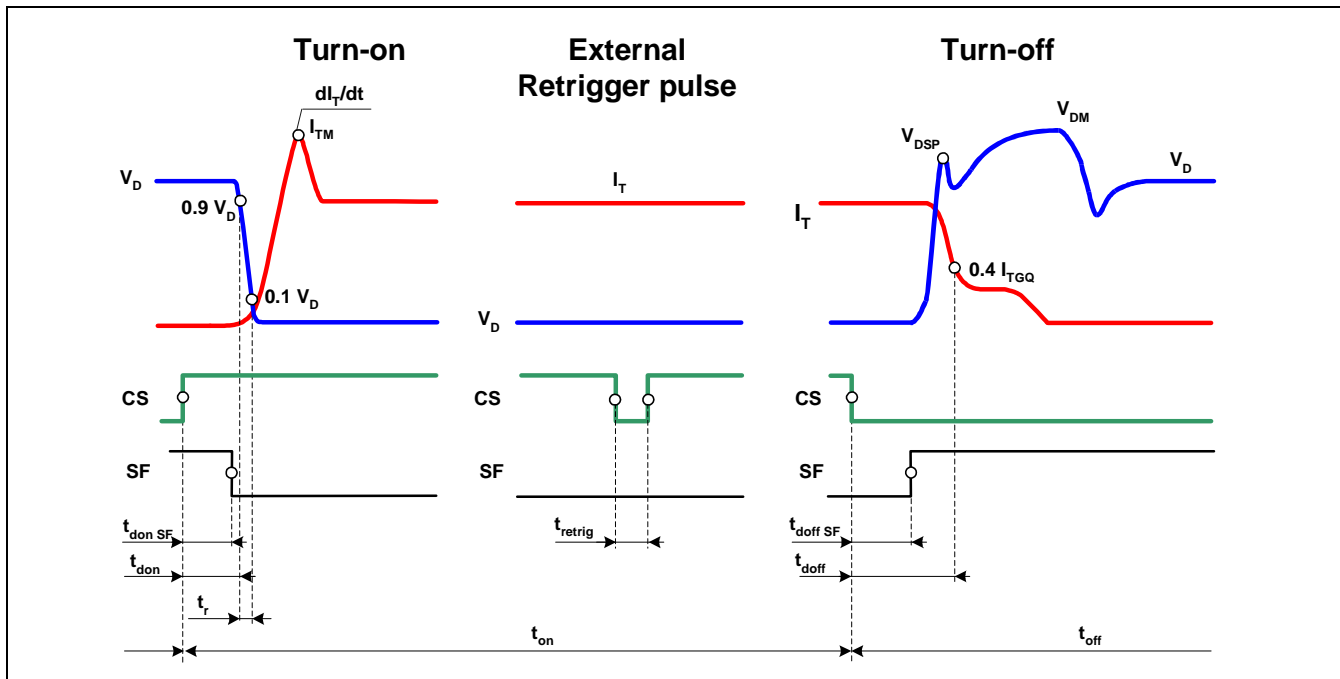


Fig. 14 General current and voltage waveforms with IGCT - specific symbols.

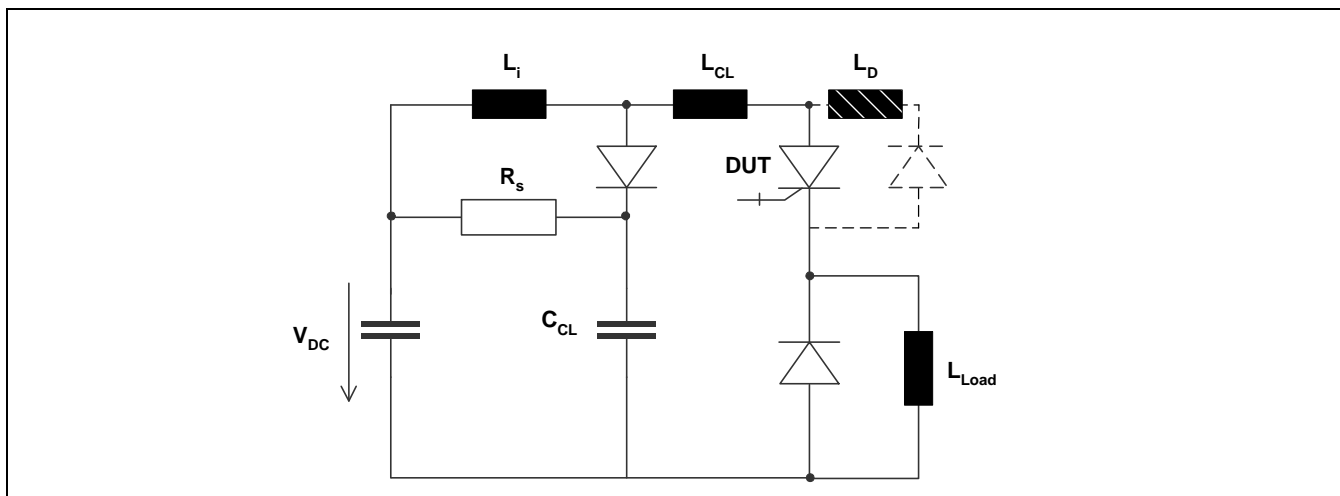


Fig. 15 Test circuit

Related application notes:

Doc. Nr	Titel
5SYA 2031	Applying IGCT Gate Units
5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors

Please refer to <http://www.abb.com/semiconductors> for actual versions.

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